

What is claimed is:

1. A solid-state imaging device comprising:
a plurality of on-chip lenses corresponding to each sensor portion in an imaging region;

wherein a center of reduction magnification of exit pupil correction which is performed for said plurality of on-chip lenses is set to a position deviated from a center of said imaging region.

2. A solid-state imaging device according to claim 1, wherein a surface of said sensor portion or an opening of a light shielding film is in an asymmetrical shape.

3. A solid-state imaging device according to claim 1, wherein the reduction magnification of said plurality of on-chip lenses is constant or is varied with respect to a distance from said center of the reduction magnification.

4. A solid-state imaging device according to claim 1, wherein the reduction magnification of said plurality of on-chip lenses is varied continuously or in a step shape with respect to a distance from said center of the reduction magnification.